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APPLICANTS

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\*\* CONTINUING DATA *None - MDP 1/16/2006*

\*\* FOREIGN APPLICATIONS *MDP 1/16/2006*  
 JAPAN 2003-088221 03/27/2003

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE <i>Michael</i>	INITIALS		
Verified and Acknowledged				

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TITLE  
 Semiconductor device having a plurality of stacked semiconductor chips with positions of chip-selecting terminals being different from each other.

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